

## Personnel

- Jamieson (JO)
- Ricardo (RR), 4/10 – 6/3
- Satish (SD), not available 5/15-19 & 6/1-10
- Levan (LB)

## Questions/Issues/Concerns as of 4/30/02:

- ☐ Need to know, will have one full OCT (ie SS mapping onto OCT, personalities...)?
- ☐ Need L3/VRB readout at CTS.
- ☐ Would like 3 DW & 5 SW MBs (10 trigger sectors) at CTS, I think could live with 2 SW MBs (4 sectors). How many DW/SW MBs can we have at CTS?
- ☐ JTAG or Compac Flash in CTS?
- ☐ How do we determine L1 pipeline depth at DFEA, CTOC, CTTT, etc.? Collider data, look at occupancies? ...
- ☐ While processing L2 or L3, ignore all incoming data, once done with L2/3 algos, go back to normal 'data acquisition' mode.
- ☐ Firmware versioning register, can wait.

# L1CTT EndGame

Dates	Tasks to be completed
5/6/2002	1. L1DFEA outputs via DP incl TVs in DFEA and occ (JO) 2. L1CTOC outputs via DP incl occ (RR) 3. L1CTOC L3-output via DP incl proper L1-output upon L1 Accept (RR,SD) 4. CTTT outputs via DP incl occ (LB) 5. FSCL from CTS to TM/L1Muon (JO,LB) 6. Status of download infrastructure to platform (??)
5/8/2002	1. DP/TV -> L1DFEA (2DB/1MB) -> L1CTOC (1) (JO,RR,LB)
5/9/2002	1. DP/TV -> L1DFEA (2DB/1MB) -> L1CTOC (1) -> L1CTTT (JO,RR,LB) 2. AFE -> Mix -> L1DFEA (2DB/1MB) -> L1CTOC (1) (JO,RR,LB)
5/10/2002	1. AFE -> Mix -> L1DFEA (2DB/1MB) -> L1CTOC (1) -> L1CTT -> TM (ALL) 2. Green light for platform downloads (??) [ 3. Standalone L3 Sender from platform (SD) -- need software?]
<b>5/13/2002</b>	<b>CTS Test SCL/AFE/Mix/L1DFEA/L1CTOC/CTTT/TM</b>
5/13/2002	1. L2DFEA output to transmit L1 Accept to L1CTOC (JO) 2. Understand TM downloads & how to put L1CTT in global runs (LB)
5/15/2002	1. CTS chain reading out L1CTOC to L3 upon real L1 Accepts (ALL)
5/17/2002	1. Download of full octant (which one? AFE personalities?) (ALL)
5/20/2002	1. How do we put L1CTT in global run? (LB) 2. Readout of L1CTOC to L3, need software to look at the data (RR,SD,LB) 3. Readout of L1CTTT to L3, need software to look at the data (RR,SD,LB)
5/27/2002	1. L1CTT one octant in global or test run with all L3 readouts (ALL)
<b>6/3/2002</b>	<b>First Octant Test of L1CTT on platform</b>